一种低功耗高电源抑制比亚阈值基准电压源

杨艺丹,赵梦恋,刘胜,董阳涛,吴晓波 (浙江大学 超大规模集成电路设计研究所,浙江 杭州 310027)

摘要:本文针对传统带隙基准功耗高、电源抑制比低的问题,提出了一种适用于能量采集、无线传感网络等的低功耗、高电源抑制比和宽电源电压范围的亚阈值基准电压源.该基准源通过采用非对称共栅极自级联电流镜结构结合负反馈提高了电压源的电源抑制比和线性调整率.电路采用 Global Foundry $0.18~\mu$ m BCD 工艺设计,仿真结果表明:基准电压源的电源电压范围为 2.7~5.5 V,输出电压为 1.2 V,功耗最低可达 446.2 nW;在 3.3 V 电源电压下、-40~125°C 的温度范围内,温度系数为 30.3 ppm;室温下线性调整率低至 0.013%/V;在 100 Hz 处电源抑制比为-75 dB.

关键词: 低功耗; 高电源抑制比; 亚阈值; 共栅极自级联电流镜

中图分类号: TN402

文献标识码: A

文章编号: 1000-7180(2018)08-0109-04

A Low Power and High Power Supply Rejection Ratio Voltage

Reference Based on Subthreshold

YANG Yi-dan, ZHAO Meng-lian, LIU Sheng, DONG Yang-tao, WU Xiao-bo (Institute of Very Large Scale Integrated Circuits Design, Zhejiang University, Hangzhou 310027 China)

Abstract: To solve the problem of high power consumption and low power supply rejection ratio(PSRR) in traditional bandgap reference, a subthreshold reference voltage source for low power consumption, high PSRR and wide power supply voltage range is presented in this paper, applying for systems such as energy harvesting and wireless sensor networks, etc. The reference enhances the PSRR and linearity regulation of the voltage source through the Asymmetric Self-Cascode current mirror andnegative feedback. Designed in the Global Foundry 0.18 μm BCD process, the simulation results show that the reference circuit is able to operate under a supply voltage range of 2.7~5.5 V, the output reference voltage is 1.2 V and the lowest power consumption is 446.2 nW. Under 3.3 V power supply voltage, the temperature coefficient is 30.3 ppm in a temperature range from -40°C to 125°C. The linear adjustment rate at room temperature is 0.013%/V. The PSRR is -75 dB at 100 Hz frequency.

Key words: low power; high PSRR; subthreshold; asymmetric self-cascode current mirror

作者简介:

杨艺丹 女,(1993-),硕士研究生.研究方向为模拟集成电路设计. 赵梦恋(通讯作者)女,博士,副教授.研究方向为模拟集成电路设计. E-mail:zhaoml@zju.edu.cn.