基于 NEON 优化技术的视频处理系统设计

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摘要: 针对软件方式的嵌入式图像处理,ARM Cortex-A 系列的处理器集成了基于 SIMD 指令架构的 NEON 协处理器引擎,能够实现一条指令同时对多个数据进行并行化处理,可以有效加速多媒体和信号处理算法.本文设计了一种基于 FPGA 的视频处理系统,充分利用 NEON 技术对 Sobel 算法进行优化加速,并与普通软件方法实现算法的方式进行对比,结果显示,采用 NEON 技术后图像处理加速显著,且视频流畅度好.

关键词: NEON 技术; SIMD 指令架构; Sobel 算法

The Design of Video Processing System Based on NEON Optimization Technique

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Abstract: Aimed at ARM-based embedded image processing, the ARM Cortex-A series of processors are integrated the NEON coprocessor engine which is based on the SIMD instruction architecture, enabling an instruction to parallelize multiple data simultaneously, which can accelerate multimedia and signaling processing algorithms (such as video codecs, image processing techniques) effectively. In this paper, we design a video processing system based on FPGA, which can make full use of NEON technology to optimize the Sobel algorithm, compared with the way of ordinary software method to implement the algorithm, the results show that NEON technology accelerates image processing significantly, decoded the video more fluent.

Key words: NEON technology; SIMD instruction architecture; sobel algorithm

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