

开环相位调制器实现技术

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摘要: 为了提高极坐标发射机可以支持的数据率, 研究了宽带开环相位调制器的实现技术, 并基于 65 nm CMOS 工艺实现了该款开环相位调制器电路, 其版图面积仅为 $0.2 \mu\text{m}^2$, 功耗为 13 mW, 平均相位步长为 0.703° , DNL 为 0.525LSB.

关键词: 相位调制器; 宽带; 开环; 发射机

Implementation of Open Loop Phase Modulator

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Abstract: In order to improve the polar transmitter can support the implementation of data rate, this paper studies the technology of broadband open loop phase modulator, and based on 65nm CMOS technology to achieve the open-loop phase modulator circuit, the chip area is only $0.2\mu\text{m}^2$, power is 13mW, the average phase step DNL for 0.525LSB.

Key words: Phase Modulator; Wideband; Open Loop; Transmitter

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