

基于节点活性的硬件木马检测方法

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摘要: 本文提出了一种基于节点活性的硬件木马检测方法, 针对电路中的低活性节点生成测试向量, 结合多参数旁路检测方法, 实现对硬件木马的检测. 以 AES 为目标电路并植入硬件木马, 进行仿真及 FPGA 实验, 实验结果表明与随机测试向量相比, 本文生成的测试向量可将木马节点的翻转概率提高一个数量级、木马检测灵敏度分别提高 6.75% (仿真)、77.4% (FPGA), 硬件木马的检测精度达到 10^{-4} .

关键词: 芯片安全; 硬件木马检测; 节点活性; 测试向量生成

A Novel Hardware Trojan Detection Method

Based on Node Activity State

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Abstract: In this paper, we propose a hardware Trojan detection method based on nodes activity state, in view of the low activity nodes of original circuit generate test vectors, Combined with the method of multi-parameter side channel signals to detect Hardware Trojan. The proposed method is verified by simulation and FPGA experiment carried on AES original circuit which is implanted with hardware trojan. The results show that compared with random test vector, the proposed method can improve the transition probability of the trojans of nodes an order of magnitude and increase the sensitivity of the Hardware Trojan detection by 6.75%(simulation), 77.4%(FPGA), detecte the Hardware Trojan whose equivalent area is as small as 10^{-4} of the total size of the circuit.

Key words: IC security; node activity state; hardware trojan detection; test pattern generation

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